

PBC-003-1273003

Seat No.

14

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M. Sc. (ECI) (Sem. III) (CBCS) Examination

November / December - 2018

Paper - 12 : Circuit Simulation & PCB Designing Tools (New Syllabus)

Faculty Code: 003

Subject Code: 1273003

Time : $2\frac{1}{2}$ Hours] [Total Marks : 70

Instructions: (1) Figures on right hand side indicate marks.

- (2) Assume suitable data if necessary.
- 1 Answer the following: (Any Seven)
 - (1) What is the use of Wire Label Mode in PROTEUS? Also draw its symbol.
 - (2) What is the use of circuit simulator?
 - (3) Draw NOR & NOT logic gate with truth table.
 - (4) What is the full form of PCB? Also explain working of PCB.
 - (5) Name the given symbols. \bigcirc \bigcirc \bigcirc \bigcirc
 - (6) What is the use of Graph Mode in PROTEUS? Also draw its symbol.
 - (7) Draw any pattern for Capacitor and IC7805.
 - (8) What is the full-form of ISIS and ARES in PROTEUS?
 - (9) What is the use of DipTrace Software?
 - (10) Draw the symbol of Diode and Op-Amp.
- 2 Answer the following: (Any Two)
 - (1) Draw & explain Half Wave Rectifier circuit.
 - (2) Write a note on GENERAL EDITING FACILITIES 7 in PROTEUS.
 - (3) Draw all symbols of 2D GRAPHICS and write use of each of them.

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3	Answer the following:		
	(1)	What is the use of Oscilloscope, Function Generator,	5
		AC-DC Ammeter and AC-DC Voltmeter?	
	(2)	Identify the following symbols. Draw a circuit by	5
		using them & also explain its working in PROTEUS.	
		0 —?	
	(3)	Draw an AND & NOT logic gate circuit using	4
		transistor.	
		OR	
3	Answer the following:		
	(1)	Draw & explain 5V regulated power supply.	5
	(2)	Give basic introduction about DipTrace. Also explain	5
		all Program Modules that are available in Dip Trace.	
	(3)	Identify the following symbols & explain working	4
		of them in PROTEUS:	
		(4)	
4	Answer the following:		
	(1)	Write a function of Junction Dot Mode and Terminal	7
	` /	Mode in PROTEUS. What is the use of Rotate and	
		Mirror Icons in PROTEUS? Also draw a symbol of each.	
	(2)	Explain about two types of windows used in	7
		PROTEUS.	
5	Answer the following: (Any Two)		
	(1)	Design & explain Decade Counter using J-K flip-flop.	7
	(2)	Write a short note on CO-ORDINATE SYSTEM in	7
		PROTEUS.	
	(3)	Explain Inverting and Non-Inverting Operational	7
		Amplifier.	
	(4)	Write a note on ANNOTATING THE DIAGRAM.	7